

DXGI_FORMAT CheatSheet

Example name: DXGI_FORMAT_R8G8B8A8_UNORM

Version 1.0 © 2010-04-29 Adam Sawicki

	UNKNOWN			
8	R8_TYPELESS	A8_UNORM	R8_UINT R8_UNORM	R8_SINT R8_SNORM
16	R16_TYPELESS	R16_FLOAT D16_UNORM	R16_UINT R16_UNORM	R16_SINT R16_SNORM
32	R32_TYPELESS	R32_FLOAT D32_FLOAT	R32_UINT	R32_SINT
8+8 = 16	R8G8_TYPELESS		R8G8_UINT R8G8_UNORM	R8G8_SINT R8G8_SNORM
16+16 = 32	R16G16_TYPELESS	R16G16_FLOAT	R16G16_UINT R16G16_UNORM	R16G16_SINT R16G16_SNORM
32+32 = 64	R32G32_TYPELESS	R32G32_FLOAT	R32G32_UINT	R32G32_SINT
24+8 = 32	R24G8_TYPELESS	D24_UNORM_S8_UINT	R24_UNORM_X8_TYPELESS	X24_TYPELESS_G8_UINT
32+32+32 = 96	R32G32B32_TYPELESS	R32G32B32_FLOAT	R32G32B32_UINT	R32G32B32_SINT
5+6+5 = 16			B5G6R5_UNORM	
5+5+5+1 = 16			B5G5R5A1_UNORM	
8+8+8+8 = 32	R8G8B8A8_TYPELESS	R8G8B8A8_UNORM_SRGB	R8G8B8A8_UINT R8G8B8A8_UNORM	R8G8B8A8_SINT R8G8B8A8_SNORM
16+16+16+16 = 64	R16G16B16A16_TYPELESS	R16G16B16A16_FLOAT	R16G16B16A16_UINT R16G16B16A16_UNORM	R16G16B16A16_SINT R16G16B16A16_SNORM
32+32+32+32 = 128	R32G32B32A32_TYPELESS	R32G32B32A32_FLOAT	R32G32B32A32_UINT	R32G32B32A32_SINT
32+8+24 = 64	R32G8X24_TYPELESS	D32_FLOAT_S8X24_UINT	R32_FLOAT_X8X24_TYPELESS	X32_TYPELESS_G8X24_UINT
10+10+10+2 = 32	R10G10B10A2_TYPELESS	R10G10B10A2_UINT	R10G10B10A2_UNORM	
8+8+8+8 = 32	B8G8R8A8_TYPELESS	B8G8R8A8_UNORM_SRGB		
8+8+8+8 = 32	B8G8R8X8_TYPELESS	B8G8R8X8_UNORM_SRGB		
1		R1_UNORM		
8+8+8+8 = 32		B8G8R8A8_UNORM	B8G8R8X8_UNORM	
8+8+8+8 = 32		R8G8_B8G8_UNORM	G8R8_G8B8_UNORM	
9+9+9+5 = 32		R9G9B9E5_SHAREDEXP		
11+11+10 = 32		R11G11B10_FLOAT		
10+10+10+2 = 32		R10G10B10_XR_BIAS_A2_UNORM		
Compressed	BC1_TYPELESS	BC1_UNORM	BC1_UNORM_SRGB	
Compressed	BC2_TYPELESS	BC2_UNORM	BC2_UNORM_SRGB	
Compressed	BC3_TYPELESS	BC3_UNORM	BC3_UNORM_SRGB	
Compressed	BC4_TYPELESS	BC4_UNORM	BC4_SNORM	
Compressed	BC5_TYPELESS	BC5_UNORM	BC5_SNORM	
Compressed	BC6H_TYPELESS	BC6H_UF16	BC6H_SF16	
Compressed	BC7_TYPELESS	BC7_UNORM	BC7_UNORM_SRGB	